

## **Remarks**

Applicant respectfully requests reconsideration of this application as amended. No claims have been amended. No claims have been cancelled. Therefore, claims 1-20 are presented for examination.

Claims 1-20 stand rejected under 35 U.S.C. §103(a) as being unpatentable over Blatter et al. (U.S. Patent No. 6,016,348). Applicant submits that the present claims are patentable over Blatter.

Blatter discloses a decoder conditional access system that incorporates different encryption systems for providing access to programs derived from different sources. A conditional access processor for processing encrypted program data and an associated encryption code includes a first algorithm means for decrypting the encryption code to provide an encryption key. The conditional access processor also includes a second algorithm means for encrypting the encryption key and the second encryption algorithm is different to the first encryption algorithm. A data stream representing the program is generated from encrypted program data and an associated encryption code. The encryption code is decrypted to provide an encryption key using a first algorithm. The encryption key is encrypted using a second algorithm different to the first encryption algorithm and the program data stream is formed from the encrypted program data and the encrypted encryption key. The program data stream is decrypted by selecting between a first and a second decryption algorithm and by decrypting the encryption code to provide an encryption key. The encryption key is used to decrypt the encrypted program. A storage medium data format for recording encrypted program data is also disclosed. See Blatter at Abstract.

Claim 1 of the present application recites:

A method comprising:  
receiving video data at an application program;  
transmitting the video data to one or more memory  
buffers;  
decrypting the video data; and  
monitoring a page table entry bit corresponding to each  
of the one or more memory buffers to determine whether a  
second application program has accessed the one or more  
memory buffers.

Applicant submits that Blatter does not disclose or suggest a process of monitoring a page table entry corresponding to a memory buffers to determine whether a second application program has accessed the memory buffer. However, the Examiner relies on Blatter at col. 4 (line 44) to col. 5(line 15) as disclosing such a feature. See Office Action at page 2, paragraph 4.

The passage relied on by the Examiner discloses:

In response to control signal C, mux 37 selects either the transport stream from unit 35, or in a playback mode, a data stream retrieved from storage device 90 via store interface 95. In normal, non-playback operation, the data packets comprising the program that the user selected to view are identified by their PIDs by selection unit 45. If an encryption indicator in the header data of the selected program packets indicates the packets are encrypted, unit 45 provides the packets to decryption unit 50. Otherwise unit 45 provides non-encrypted packets to transport decoder 55. Similarly, the data packets comprising the programs that the user selected for storage are identified by their PIDs by selection unit 47. Unit 47 provides encrypted packets to decryption unit 50 or non-encrypted packets to mux 110 based on the packet header encryption indicator information.

The pre-loaded PIDs are used in units 47 and 45 to identify the data packets that are to be stored and the data packets that are to be decoded for use in providing a video image. The pre-loaded PIDs are stored in look-up tables in units 45 and 47. The PID look-up tables are memory mapped to encryption key tables in units 45 and 47 that

associate encryption keys with each pre-loaded PID. The memory mapped PID and encryption key look-up tables permit units 45 and 47 to match encrypted packets containing a pre-loaded PID with associated encryption keys that permit their decryption. Non-encrypted packets do not have associated encryption keys. Units 45 and 47 provide both identified packets and their associated encryption keys to decryptor 50. The PID look-up table in unit 45 is also memory mapped to a destination table that matches packets containing pre-loaded PIDs with corresponding destination buffer locations in packet buffer 60. The encryption keys and destination buffer location addresses associated with the programs selected by a user for viewing or storage are pre-loaded into units 45 and 47 along with the assigned PIDs by controller 115.

Blatter at col. 4 (line 44) to col. 5(line 15).

Applicant submits that nowhere in the passage of Blatter relied on by the Examiner is there disclosed or suggested a process of monitoring a page table entry corresponding to a memory buffers to determine whether a second application program has accessed the memory buffer. Instead, the passage includes a description of how look-up tables are memory mapped to encryption key tables that associate encryption keys with pre-loaded PIDs. Thus, the Examiner's construal of Blatter does not suggest monitoring a page table entry to determine whether a second application program has accessed a memory buffer. As a result, claim 1 is patentable over Blatter.

Claims 2-8 depend from claim 1 and include additional features. Thus, claims 2-8 are also patentable over Blatter.

Claim 9 recites:

A computer system comprising:  
an application to receive data content;  
a memory device to store the data content;  
a decoder to decode the content; and  
a decryption module to decrypt the data content, and to monitor access to the memory device to determine if memory buffers storing the data content have been

accessed by a second application prior to the decoding of the data content.

For the reasons described above with respect to claim 1, claim 9 is patentable over Blatter. Because claims 10-13 depend from claim 9 and include additional features, claims 10-13 are also patentable over Blatter.

Claim 14 recites:

An article of manufacture including one or more computer readable media that embody a program of instructions, wherein the program of instructions, when executed by a processing unit, causes the processing unit to:

receive video data at an application program;  
transmit the video data to one or more memory buffers  
decrypt the video data; and  
monitor a page table entry bit corresponding to each of the one or more memory buffers to determine whether a second application program has accessed the one or more memory buffers.

Thus, for the reasons described above with respect to claim 1, claim 14 is patentable over Blatter. Because claims 15-20 depend from claim 14 and include additional features, claims 15-20 are also patentable over Blatter.

Applicants respectfully submit that the rejections have been overcome and that the claims are in condition for allowance. Accordingly, applicants respectfully request the rejections be withdrawn and the claims be allowed.

The Examiner is requested to call the undersigned at (303) 740-1980 if there remains any issue with allowance of the case.

Please charge any shortage to our Deposit Account No. 02-2666.



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Respectfully submitted,

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